#### Session Types Towards safe and fast reconfigurable programming HEART 2012

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#### • Parallel and heterogeneous architectures

- Combines parallelism and specialisation, eg FPGA
- Efficient use of computing resources
- Difficult to program (correctly)
- One source of error of parallelising
  - Communication mismatch (send-receive)
  - Communication deadlocks

- Message passing communication
  - scalable, commonly used
- MPI (Message-Passing Interface)
  - common for communication in parallel computers
- Communication mismatch and deadlocks
  - lead to program error

# Motivating example

if (rank == 0) { // Program 0
 MPI\_Send(a, 5, MPI\_INT, 1, TAG, MPI\_COMM\_WORLD);
 MPI\_Recv(b, 5, MPI\_INT, 1, TAG, MPI\_COMM\_WORLD);
} else if (rank == 1) { // Program 1
 MPI\_Send(b, 5, MPI\_INT, 0, TAG, MPI\_COMM\_WORLD);
 MPI\_Recv(a, 5, MPI\_INT, 0, TAG, MPI\_COMM\_WORLD);
}

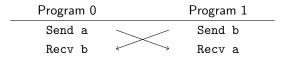


Figure: Interaction of two processes with a deadlock.

- An intuitive programming framework and toolchain
  - For message-passing parallel programming
  - Based on formal and explicit interaction protocol
- Advanced communication topologies for computer clusters
- Case study comparing framework with existing tools

### Proposal: Session types

- Session Types [Honda et al. ESOP'98, POPL'08]
  - Typing system for communication
  - Ensure compatible communication (send-receive) by typing
    - Sequence of send and receive
    - Also types flow-control constructs (eg. loops, if-then-else)
- Ideal to integrate into programming language

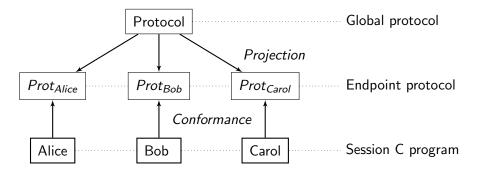


Figure: Incompatible.

Figure: Compatible.

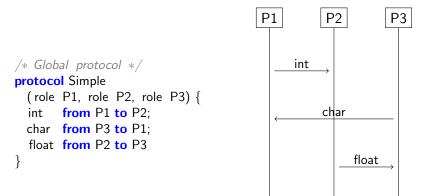
- Minimal extension of C language to support Session Types
- General communication-based framework
  - Safe distributed parallel programming
  - High performance applications
- Focussed on computer cluster communication

## Session C toolchain and key reasoning



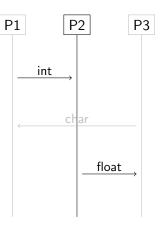
- Design protocol in global view
- Output Automatic projection to endpoint protocol, algorithm preserves safety
- Write program according to endpoint protocol
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- $\bullet \Rightarrow$  Safe program by design

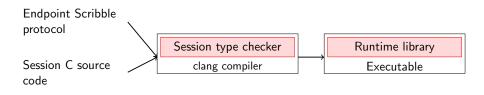
## Scribble protocol specification language: Example



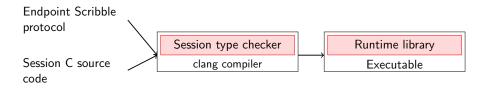
```
/* Endpoint protocol for P2 */
protocol Simple at P2
  (role P1, role P3) {
    int from P1;
    float to P3;
}
    Projection of Simple with respect
    to P2
    E. b. i.e. and bf
```

```
    Endpoint protocol from 
perspective of P2
```



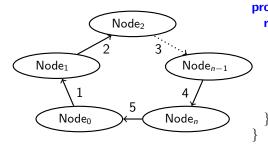


- User input protocol and C source code
- Session C framework
  - Runtime/communication API
  - Session Type checker



- Static analyser for source code
- Verify source code conforms with protocol specification
- Protocol extracted based on usage of API

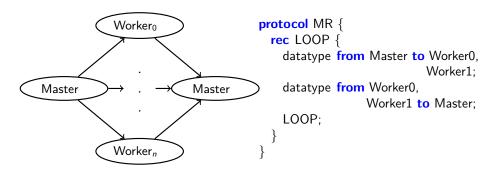
- Topology safe if can be described in framework
  - Subject to global protocol well-formedness conditions
- Examples: Ring topology and map-reduce



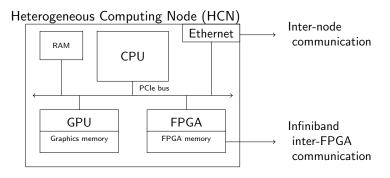
protocol Ring {
 rec LOOP {
 datatype from Node0 to Node1;
 datatype from Node1 to NodeN;
 // Wrap back to Node0
 datatype from NodeN to Node0;
 }
}

datatype **from** NodeN **to** Node0; LOOP;

### Topologies: Map-reduce



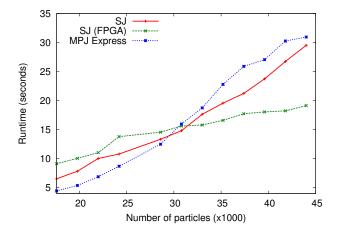
- Comparing session-enhanced programming with MPI
- Strength: Protocol known (and safe) at implementation time
- Asynchronous operation re-ordering
  - Optimisation applied to implementations
  - Correctness ensured by Session Type checker



- Heterogeneous accelerators
  - Multicore CPU
  - GPU
  - FPGA

- Communication
  - Inter-node: Ethernet
  - Inter-component: PCI
  - Inter-FPGA: Infiniband

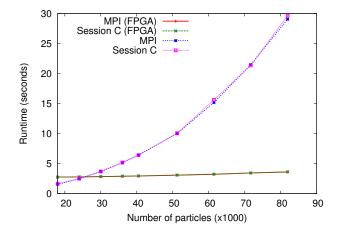
# N-body simulation accelerated by FPGA (Java)



- Session Java comparable to MPJ Express (MPI in Java)
- FPGA overhead improves with larger input size

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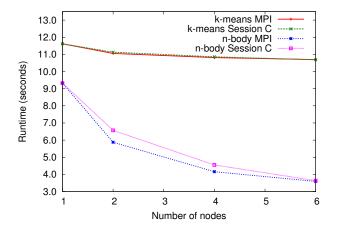
# N-body simulation accelerated by FPGA (C)



- Session C performance same as MPI
- Significant improvement with FPGA acceleration

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## Scalability: N-body simulation and K-means clustering



- Performance improve with number of nodes
- MPI and Session C converge as nodes increase

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- Session-enhanced languages (Java and C)
  - Communication safety ensured
  - Negligible performance cost
  - FPGA acceleration improves performance
  - Solution scalable

- Extending approach to include eg GPU or other hardware
- MPI-compatible runtime for multiparty session programming
- Integrate with customisable communication framework [Denholm et al., ASAP'11]

- Introduced a programming and verification framework for communication in C
- Shown advanced communication topologies for computer clusters
- Performance evaluation of framework against existing tools
  - Competitive performance

Session C runtime and type-checker http://sesscc.googlecode.com

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Appendix

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- MPI Deadlock detection by model checking techniques
  - ISP/DAMPI [Vo et al., PPoPP'09/SC'10]
  - TASS [Siegel et al., PPoPP'11]
- Our approach does not depend on testing or heuristics
  - Full guarantee of deadlock-freedom and communication-safety

```
Description of protocol

protocol P (role A, role B) {

int from A to B;

int from B to A;

}
```

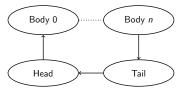
```
\Rightarrow Description of a protocol for each
                endpoints
   protocol P at A (role B) {
      int to B;
      int from B;
   }
   protocol P at B(role A) {
      int from A;
      int to A;
   }
```

```
#include <libsess.h>
int main() { // A session C program
  session *s: int ival. sum = 0:
  // Start a session that follows the protocol "Protocol_Endpoint"
  join_session (&argc, &argv, &s, "Protocol_Endpoint.spr");
  role Bob = s->get_role(s, "Bob"); // Get role handle
  send_int (Bob, 42); // Send int to Bob
  while (i < 3) {
    recv_int (Bob, &ival); // Recv int from Bob
   sum += ival;
  }
  send_int (Bob, sum); // Send int to Bob
  end_session (s); // End a session
  return 0;
```

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Ring topology: full example

- Input segmented to *n* parts
- Results shifted right until all nodes worked on all segments



N-node ring topology

```
protocol Nbody /* Global protocol */
  (role Head, role Body, role Tail) {
  rec NrOfSteps {
    rec SubCompute {
        particles from Head to Body;
        particles from Body to Tail;
        particles from Tail to Head;
        SubCompute; }
}
```

```
NrOfSteps; }
```

/\* Endpoint Protocol \*/
protocol Nbody at Body
 (role Head, role Tail) {
 rec NrOfIters {
 rec SubCompute {
 particles from Head;
 particles to Tail;
 }
}

SubCompute; }

NrOflters;}

/\* Implementation of Body worker \*/ particle\_t \*ps, \*tmp\_ps; while (iterations  $++ < ITERS_NR$ ) { while (rounds $++ < NODES_NR$ ) { send\_particles(Tail, tmp\_ps); // Update veclocities compute\_forces(ps, tmp\_parts ,...); recv\_particles (Head, &tmp\_ps); { // Update positions // by received velocities compute\_positions(ps, pvs, ... ); }

Asynchronous reordering

- Some synchronous operations can be **safely** reordered [Mostrous et al., ESOP'09]
- Pipelines impossible in 'strict' multiparty session types, possible with asynchronous subtyping
- Safe pipeline improves performance, more scalable

Figure: MPST.			Figure: Asynchronous subtyping				
А.	senu	B.Tecv	C:recv		A:send	B:send	C:send
	<u> </u>	Stage II B:recv	Stage III		Stage I	Stage II	Stage III

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Stage I Stage II Stage III	Stage I Stage II Stage III						
A:send→B:recv							
B:send C:recv	A:send B:send C:send						
A:recv							

Figure: MPST.

Figure: Asynchronous subtyping.

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Stage I St	age II Stage III	Stage I	Stage II	Stage III
$A: \texttt{send} \rightarrow B:$	recv			
B:	A:send	B:send	C:send	
A:recv	C:send			

Figure: MPST.

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Stage I	Stage II Stage III	Stage I	Stage II	Stage III	
A:send $\rightarrow$ B:recv					
	$B: \texttt{send} \rightarrow \texttt{C}: \texttt{recv}$	A:send	B:send	C:send	
->A:recv	C:send ->				

Figure: MPST. Figure: Asynchronous subtyping.

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Stage I	Stage II	Stage III		Stage I	Stage II	Stage III
A:send-	B:recv					
	B:send-	→C:recv	```	A:send	B:send	C:send C:recv
->A:recv		C:send->		A:recv	<sup>\L</sup> B:recv	C:recv

Figure: MPST.

Figure: Asynchronous subtyping.

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